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Agenda | Quick reminder about ARM platforms | User perspective: booting with the Device Tree | Basic Device Tree syntax and compilation | Simple example of

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Device Tree fragment |
Overall organization of
a Device Tree |
Examples of Device
Tree usage | General
considerations about
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Kernel, drivers and
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Device Tree for Dummies - Bootlin

User perspective:

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booting with a Device Tree | The kernel no longer contains the description of the hardware, it is located in a separate binary: the device tree blob | The bootloader loads two binaries: the kernel image and the DTB | Kernel image remains ulmage or zImage | DTB located in arch/arm/boot/dts, one per board | The bootloader passes the DTB address through

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The conversion of the ARM Linux kernel over to the Device Tree as the mechanism to describe the hardware has been a significant change for ARM kernel developers. Nowadays, all developers porting the L...

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Device tree compiler and its source code located at [scripts/dtc/](#).

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On ARM all device tree source are located at `/arch/arm/boot/dts/`.

The Device Tree Blob(.dtb) is produced by the compiler, and it is the binary that gets loaded by the bootloader and parsed by the kernel at boot time.

Device Tree Tutorial (ARM) - Linux Kernel For Newbies

A device tree is a tree structure used to

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describe the physical hardware in a system. Each node in the tree describes the characteristics of the device being represented. The purpose of the device tree is to describe device information in a system that cannot necessarily be dynamically detected or discovered

Introduction to Device Trees - NXP

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